

<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b> <i>B4</i>  (Use Several Sheets if Necessary)	Atty Docket No. BRCMP002 Applicant: Chin et al. Filing Date Herewith	Application No.: Unassigned  Group <i>2136</i> Unassigned
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**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
<i>ee</i>	A	D. Vileger and V. G. Oklobdzija, " <u>Analysis Of Booth Encoding Efficiency In Parallel Multipliers Using Compressors For Reduction Of Partial Products</u> ", <i>Proceedings of the 27th Asilomar Conference on Signals, Systems and Computers</i> , pp. 781-784, 1993.
<i>w</i>	B	V. G. Oklobdzija and D. Vileger, " <u>Improving Multiplier Design By Using Improved Column Compression Tree And Optimized Final Adder In CMOS Technology</u> ", <i>IEEE Transactions on VLSI Systems</i> , Vol.3, No.2, June, 1995, 25 pages.
<i>w</i>	C	V. G. Oklobdzija, D. Vileger, and S. S. Liu, " <u>A Method For Speed Optimized Partial Product Reduction And Generation Of Fast Parallel Multipliers Using An Algorithmic Approach</u> ", <i>IEEE Transactions on Computers</i> , Vol. 45, No.3, March 1996.
<i>w</i>	D	P. Stelling, V. G. Oklobdzija, " <u>Design Strategies for Optimal Hybrid Final Adders in a Parallel Multiplier</u> ", <i>special issue on VLSI Arithmetic</i> , Journal of VLSI Signal Processing, Kluwer Academic Publishers, Vol.14, No.3, December 1996.
<i>w</i>	E	P. Stelling, V. G. Oklobdzija, " <u>Achieving Multiply-Accumulate Operation in the Multiply Time</u> ", <i>Thirteenth International Symposium on Computer Arithmetic</i> , Pacific Grove, California, July 5 - 9, 1997.
<i>w</i>	F	C. Martel, V. G. Oklobdzija, P. Stelling, " <u>Design Strategies For Optimal Multiplier Circuits</u> " <i>IEEE Transaction on Computers</i> , in press, Vol. 47, No. 3, March 98.
<i>a</i>	G	A. A. Farooqui, V. G. Oklobdzija, " <u>General Data-Path Organization of a MAC unit for VLSI Implementation of DSP Processors</u> ", <i>1998 IEEE International Symposium on Circuits and Systems</i> , May 31 - June 3 1998, Monterey, California.
<i>e</i>	H	<del><a href="http://www.bluesteelnet.com/product.html">http://www.bluesteelnet.com/product.html</a></del> <span style="border: 1px solid black; padding: 2px;">Undated</span>
<i>e</i>	I	<del><a href="http://sg.rainbow.com/products/cs_1.html">http://sg.rainbow.com/products/cs_1.html</a></del> <span style="border: 1px solid black; padding: 2px;">Undated</span>
Examiner <i>ee ee</i>	Date Considered	<i>2/10/09</i> <span style="border: 1px solid black; padding: 2px;">5/13/2008</span>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.